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(54) **DISPLAY SCREEN CONTROL METHOD AND TERMINAL**

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(57) **ABSTRACT**

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A display screen control method and a terminal for monitoring whether a terminal satisfies a low-power running condition, where a display screen of the terminal is an organic light emitting display screen, and each pixel unit of the organic light emitting display screen includes at least one red subpixel, at least one blue subpixel, and at least one green subpixel, and decreasing a frequency of any one or two of a drive signal of the red subpixel, a drive signal of the green subpixel, and a drive signal of the blue subpixel when it is detected that the terminal satisfies the low-power running condition.

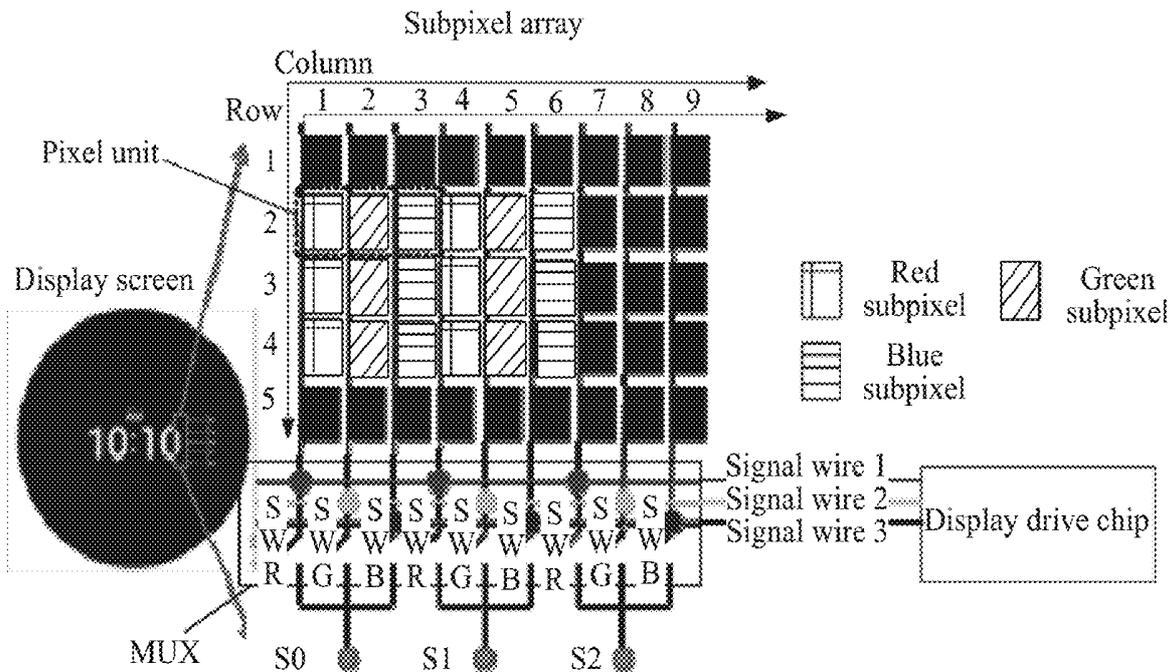
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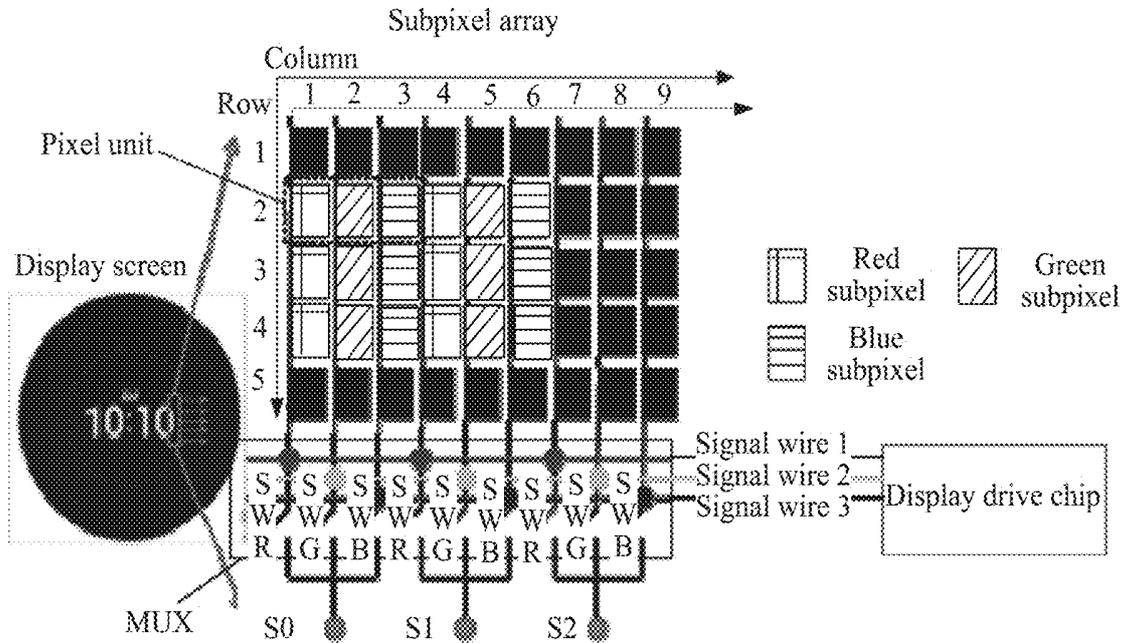


FIG. 1

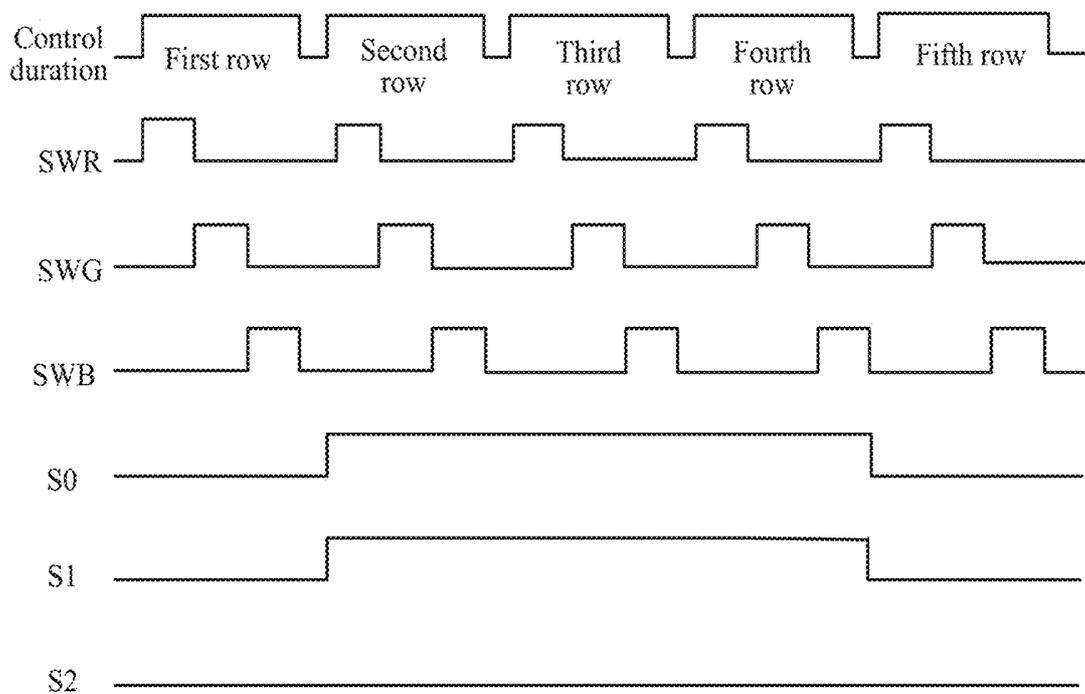


FIG. 2

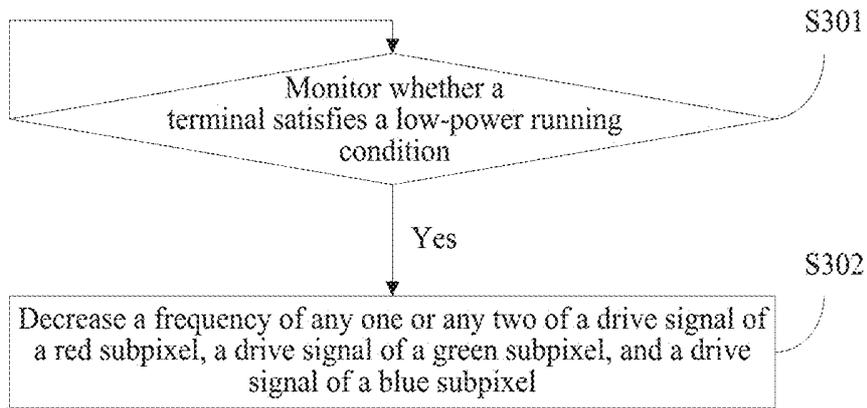


FIG. 3

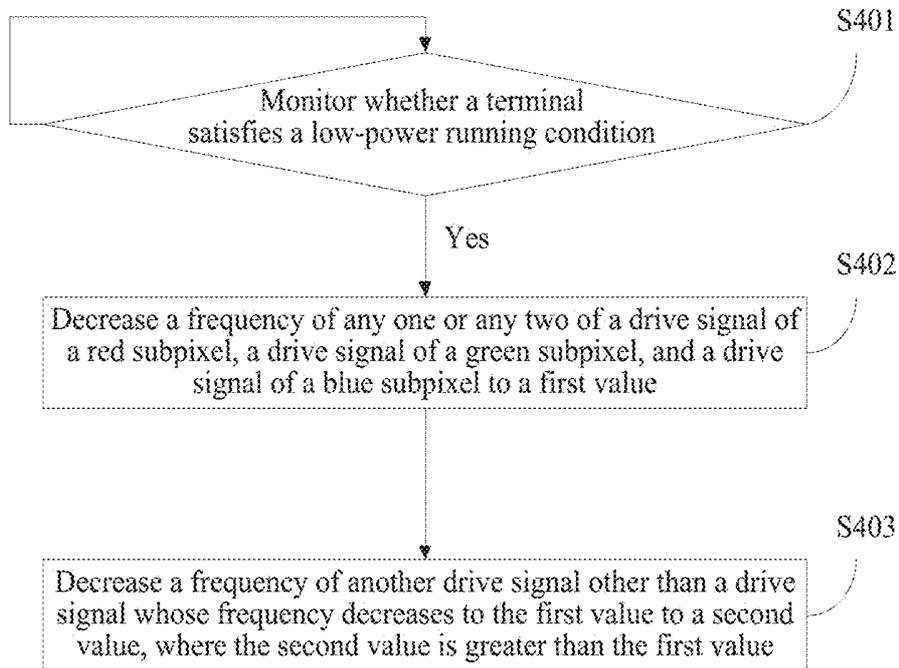


FIG. 4

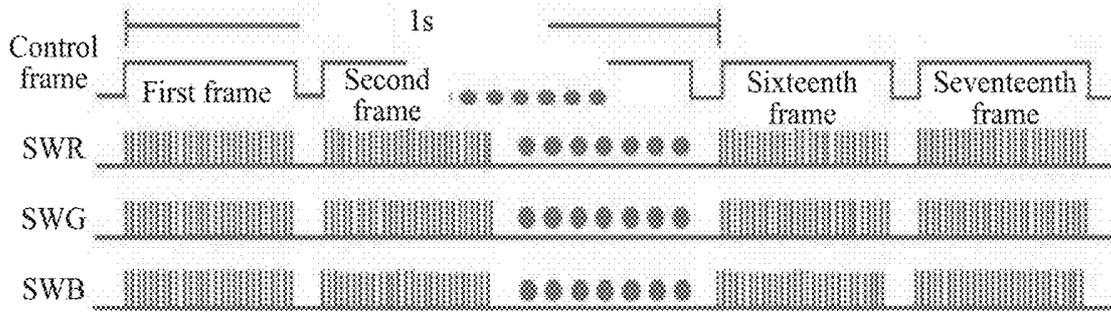


Fig. 5(a)

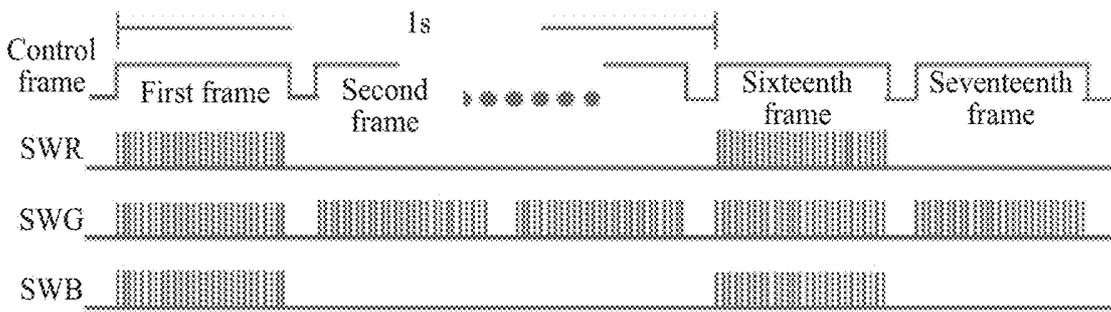


FIG. 5(b)

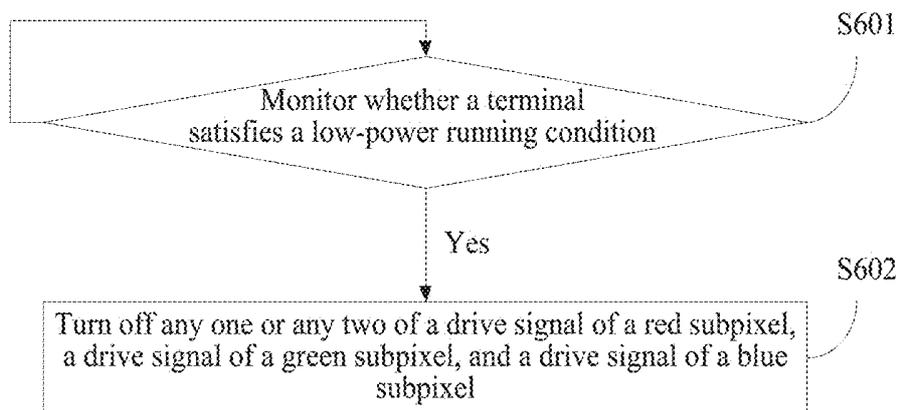


FIG. 6

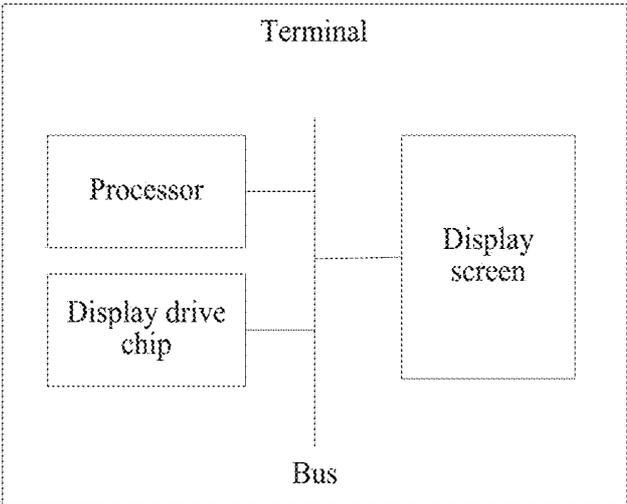


FIG. 7

## DISPLAY SCREEN CONTROL METHOD AND TERMINAL

[0001] This application claims priority to Chinese Patent Application No. 201710222055.0, filed with the Chinese Patent Office on Apr. 6, 2017 and entitled "DRIVE CIRCUIT OF AMOLED DISPLAY SCREEN", which is incorporated herein by reference in its entirety.

### TECHNICAL FIELD

[0002] This application relates to the control field, and in particular, to a display screen control method and a terminal.

### BACKGROUND

[0003] An active-matrix organic light emitting diode (active-matrix organic light emitting diode, AMOLED) is a commonly used component of a display screen of a terminal. The AMOLED forms a subpixel of the display screen. For example, a red AMOLED is a red subpixel, a green AMOLED is a green subpixel, and a blue AMOLED is a blue subpixel. Light emitted by a pixel on the display screen is synthesized by light (that is, red light, green light, and blue light) emitted after the red subpixel, the green subpixel, and the blue subpixel are lit. To decrease power consumption and increase a battery life of the terminal, currently, most terminals turn off some AMOLEDs in a standby mode. In other words, the terminals light only some subpixels.

[0004] However, research shows that even if only the some subpixels are lit in the standby mode, a display drive chip used to drive the subpixels to be lit is still a largest source of power consumption of the terminals.

[0005] Therefore, how to decrease power consumption of the display drive chip becomes a problem that urgently needs to be resolved currently.

### SUMMARY

[0006] This application provides a display screen control method and a terminal, and an objective is to resolve a problem of how to decrease power consumption of a display drive chip.

[0007] To achieve the foregoing objective, this application provides the following technical solutions:

[0008] A first aspect of this application provides a display screen control method, including: monitoring whether a terminal satisfies a low-power running condition, where a display screen of the terminal is an organic light emitting display screen, and each pixel unit of the organic light emitting display screen includes at least one red subpixel, at least one blue subpixel, and at least one green subpixel; and decreasing a frequency of any one or two of a drive signal of the red subpixel, a drive signal of the green subpixel, and a drive signal of the blue subpixel when it is detected that the terminal satisfies the low-power running condition. Decreasing the frequency of any one or two of the drive signals can decrease a function of a display drive chip. Therefore, power consumption can be reduced.

[0009] A second aspect of this application provides a terminal, including a display screen, one or more processors, and a display drive chip. The display screen is an organic light emitting display screen, and each pixel unit of the organic light emitting display screen includes at least one red subpixel, at least one blue subpixel, and at least one green subpixel. The display drive chip is configured to drive, by

using drive signals, the at least one red subpixel, the at least one blue subpixel, and the at least one green subpixel to be lit. The processor is configured to monitor whether the terminal satisfies a low-power running condition, and send a control instruction to the display drive chip when it is detected that the terminal satisfies the low-power running condition, where the control instruction is used to instruct to decrease a frequency of any one or two of a drive signal of the red subpixel, a drive signal of the green subpixel, and a drive signal of the blue subpixel. The terminal can reduce power consumption by decreasing a frequency of a drive signal.

[0010] In an implementation, the decreasing a frequency of any one or two of a drive signal of the red subpixel, a drive signal of the green subpixel, and a drive signal of the blue subpixel includes: decreasing the frequency of any one or two of the drive signal of the red subpixel, the drive signal of the green subpixel, and the drive signal of the blue subpixel to a first value. Further, the method further includes: decreasing a frequency of another drive signal other than a drive signal whose frequency is decreased to the first value to a second value, where the second value is greater than the first value. Frequencies of some drive signals are decreased to the first value, and frequencies of other drive signals are decreased to the second value greater than the first value. This helps ensure normal display of the display screen to a largest extent while reducing power consumption.

[0011] In an implementation, the first value is 1 Hz, and the second value is 15 Hz.

[0012] In an implementation, the low-power running condition includes at least one of the following: an electricity quantity of the terminal is less than a preset value; the terminal is currently in a low power mode; and the terminal receives a preset instruction.

[0013] A third aspect of this application provides a display screen control method, including: monitoring whether a terminal satisfies a low-power running condition, where a display screen of the terminal is an organic light emitting display screen, and each pixel unit of the organic light emitting display screen includes at least one red subpixel, at least one blue subpixel, and at least one green subpixel; and turning off any one or two of a drive signal of the red subpixel, a drive signal of the green subpixel, and a drive signal of the blue subpixel when it is detected that the terminal satisfies the low-power running condition. Turning off some drive signals can reduce power consumption.

[0014] A fourth aspect of this application provides a terminal, including a display screen, one or more processors, and a display drive chip. The display screen is an organic light emitting display screen, and each pixel unit of the organic light emitting display screen includes at least one red subpixel, at least one blue subpixel, and at least one green subpixel. The display drive chip is configured to drive, by using drive signals, the at least one red subpixel, the at least one blue subpixel, and the at least one green subpixel to be lit. The processor is configured to monitor whether the terminal satisfies a low-power running condition, and send a control instruction to the display drive chip when it is detected that the terminal satisfies the low-power running condition, where the control instruction is used to instruct to turn off any one or two of a drive signal of the red subpixel, a drive signal of the green subpixel, and a drive signal of the

blue subpixel. The terminal can reduce power consumption by turning off some drive signals.

#### BRIEF DESCRIPTION OF DRAWINGS

**[0015]** To describe the technical solutions in the embodiments of the present invention or in the prior art more clearly, the following briefly describes the accompanying drawings required for describing the embodiments or the prior art. Apparently, the accompanying drawings in the following description show merely some embodiments of the present invention, and a person of ordinary skill in the art may derive other drawings from these accompanying drawings without creative efforts.

**[0016]** FIG. 1 is a schematic structural diagram of a display circuit in a terminal;

**[0017]** FIG. 2 is a schematic waveform diagram of driving the display circuit shown in FIG. 1 to emit light;

**[0018]** FIG. 3 is a flowchart of a display screen control method according to an embodiment of the present invention;

**[0019]** FIG. 4 is a flowchart of another display screen control method according to an embodiment of the present invention;

**[0020]** FIG. 5(a) is a schematic waveform diagram controlled by a display screen in the prior art;

**[0021]** FIG. 5(b) is a schematic waveform diagram of a display screen control method according to an embodiment of the present invention;

**[0022]** FIG. 6 is a flowchart of another display screen control method according to an embodiment of the present invention; and

**[0023]** FIG. 7 is a schematic structural diagram of a terminal according to an embodiment of the present invention.

#### DESCRIPTION OF EMBODIMENTS

**[0024]** FIG. 1 is a schematic structural diagram of a display circuit in a terminal. In FIG. 1, a correspondence between a pixel on a display screen shown on a left side and a subpixel array shown on a right side is: light emitted by each pixel on the display screen is synthesized by light emitted by at least one red subpixel, at least one green subpixel, and at least one blue subpixel. Subpixels corresponding to all pixels form the subpixel array shown on the right side in FIG. 1. Based on the foregoing correspondence, any pixel on the display screen is referred to as a pixel unit. Each pixel unit includes the at least one red subpixel, the at least one green subpixel, and the at least one blue subpixel (a part in a dashed-line box in FIG. 1) in the subpixel array.

**[0025]** Any column of the subpixel array includes subpixels in a same color. As shown in FIG. 1, subpixels in a first column of the subpixel array are all red subpixels, subpixels in a second column are all green subpixels, and subpixels in a third column are all blue subpixels. An arrangement manner of a fourth column to a sixth column and an arrangement manner of a seventh column to a ninth column are both the same as an arrangement manner of the first column to the third column. Based on the foregoing arrangement manner, in any row of the subpixel array, every three subpixels form one pixel unit on the display screen on the left side. White light emitted by a pixel is synthesized by red light emitted by a red subpixel, green light emitted by a green subpixel, and blue light emitted by a blue subpixel.

**[0026]** In FIG. 1, S0, S1, and S2 are all signal sources. Each signal source is switchably connected to a column of red subpixels, a column of green subpixels, and a column of blue subpixels by using a multiplexer (multiplexer, MUX). A display drive chip is connected to the red subpixels, the green subpixels, and the blue subpixels by using signal wires. Specifically, the display drive chip is connected to all red subpixels in the subpixel array by using a signal wire 1, is connected to all green subpixels in the subpixel array by using a signal wire 2, and is connected to all blue subpixels in the subpixel array by using a signal wire 3. A dot on each signal wire indicates that signal wires intersecting at the dot are in communication.

**[0027]** The MUX receives drive signals (represented by SWR) of the red subpixels from the display drive chip by using the signal wire 1, receives drive signals (represented by SWG) of the green subpixels from the display drive chip by using the signal wire 2, and receives drive signals (represented by SWB) of the blue subpixels from the display drive chip by using the signal wire 3. When a received drive signal of subpixels in a color is valid (at a high level), the MUX turns on a switch between the subpixels in the color and a signal source.

**[0028]** FIG. 1 shows a display status of an existing display screen in a low power mode. In the low power mode, in the subpixel array, only some subpixels in the second, the third, and the fourth rows are lit (where the lit subpixels are represented by using white blocks, and different patterns in the white blocks are used to distinguish different colors), and other subpixels are all unlit (where the unlit subpixels are represented by using black blocks). Corresponding to the display screen, only pixels displaying time on the display screen are lit (white parts), and other pixels are unlit (black parts).

**[0029]** FIG. 2 is a waveform diagram of a control signal forming a scenario in which some subpixels in the subpixel array shown in FIG. 1 are lit. In FIG. 2, a waveform in a first row is a control duration waveform. High levels successively indicate control periods of a first row of subpixels, a second row of subpixels, . . . , and a fifth row of subpixels in the subpixel array in FIG. 1.

**[0030]** SWR is a drive signal of the red subpixel in the subpixel array in FIG. 1, and a high level indicates that the drive signal is valid. SWG is a drive signal of the green subpixel in the subpixel array in FIG. 1, and a high level indicates that the drive signal is valid. SWB is a drive signal of the blue subpixel in the subpixel array in FIG. 1, and a high level indicates that the drive signal is valid.

**[0031]** S0, S1, and S2 are all signal sources. A high level indicates validity.

**[0032]** In a control period of the first row of subpixels, high levels successively occur in the drive signals SWR, SWG, and SWB. Therefore, the MUX successively communicates red subpixels, green subpixels, and blue subpixels with the signal sources. However, S0, S1, and S2 are all at low levels. Therefore, the first row of subpixels are all unlit.

**[0033]** In a control period of the second row of subpixels, high levels successively occur in the drive signals SWR, SWG, and SWB. When SWR is at the high level, the MUX respectively communicates red subpixels with corresponding signal sources. In the control period of the second row of subpixels, S0 and S1 are at high levels, and S2 is all at a low level. Therefore, red subpixels under control of S0 and S1 are lit, and red subpixels under control of S2 are unlit. A

principle that green subpixels are lit and a principle that blue subpixels are lit are the same as a principle that the red subpixels are lit.

**[0034]** A lighting principle of a third row of subpixels and a lighting principle of a fourth row of subpixels are the same as that of the second row of subpixels, and a lighting principle of the fifth row of subpixels is the same as that of the first row of subpixels. Details are not described herein again.

**[0035]** It can be learned from FIG. 1 and FIG. 2 that, for a subpixel area not to be lit in the subpixel array, a signal source in the area only needs to be invalidated. For a subpixel area to be lit in the subpixel array, in addition to validating a signal source in the area, the subpixel further needs to be driven, in cooperation with a drive signal, to be lit.

**[0036]** Which areas of subpixels are to be lit is beyond a discussion scope of this application.

**[0037]** An objective of a display screen control method provided in an embodiment of this application is to decrease power consumption of a display drive chip by decreasing a frequency of any one or two of SWR, SWG, and SWB.

**[0038]** The terminal applicable to the scenario shown in FIG. 1 includes a light emitting display screen. The light emitting display screen includes, but is not limited to, an AMOLED display screen and an organic light emitting diode (organic light emitting diode, OLED) display screen. Specifically, the terminal may be a handheld device, an in-vehicle device, a wearable device, a computing device, and user equipments (user equipment, UE) in various forms, a mobile station (mobile station, MS), and the like. For example, the terminal in this application may be a smart-watch.

**[0039]** A correspondence between a pixel on the light emitting display screen and a subpixel array may be shown in FIG. 1. However, FIG. 1 is only an example. Subpixels in the subpixel array may alternatively be arranged in another manner, for example, an RGB PenTile manner.

**[0040]** FIG. 3 shows a display screen control method according to an embodiment of this application. The method includes the following steps.

**[0041]** S301: Monitor whether a terminal satisfies a low-power running condition, and if yes, perform S302, or if no, continue to perform S301. Optionally, S301 may be performed in a preset period.

**[0042]** Specifically, the low-power running condition may include at least one of the following: an electricity quantity of the terminal is less than a preset value, the terminal is currently in a low power mode, and the terminal receives a preset instruction (for example, an instruction sent after a user selects an “ultra power saving” option on the terminal).

**[0043]** The low power mode is a mode that is started for decreasing consumption of the terminal to save power. In the low power mode, the terminal may save power by closing some applications, some hardware, some processes, and the like. For example, the terminal starts a standby mode. In the standby mode, the terminal closes some processes running in a background.

**[0044]** S302: Decrease a frequency of any one or two of SWR, SWG, and SWB.

**[0045]** Power  $P=fv^2$ , where  $f$  indicates a drive frequency,  $v$  indicates an amplitude value of a control signal, and  $c$  indicates load. It can be learned from the foregoing formula that, when  $f$  is decreased, power is also decreased.

Therefore, use of a method of decreasing a frequency of a control signal of a subpixel can decrease power consumption of a display drive chip, thereby decreasing power consumption of the terminal, and increasing a battery life of the terminal.

**[0046]** FIG. 4 is another display screen control method according to an embodiment of this application. A difference from the method shown in FIG. 3 lies in that, to further decrease power consumption, frequencies of drive signals of three channels for subpixels are all decreased.

**[0047]** Specifically, FIG. 4 includes the following steps.

**[0048]** S401: Monitor whether a terminal satisfies a low-power running condition, and if yes, perform S402.

**[0049]** S402: Decrease a frequency of any one or two of SWR, SWG, and SWB to a first value.

**[0050]** S403: Decrease a frequency of another drive signal other than a drive signal whose frequency is decreased to the first value to a second value, where the second value is greater than the first value.

**[0051]** For example, in S402, frequencies of SWG and SWB are decreased to 1 Hz, and in S403, a frequency of SWR is decreased to 15 Hz.

**[0052]** An order of S402 and S403 may be changed.

**[0053]** The following describes procedures shown in FIG. 3 and FIG. 4 by using FIG. 5 as an example.

**[0054]** In a normal working state of the terminal, frequencies of SWR, SWG, and SWB are all 60 Hz. The display drive chip monitors whether the terminal enters the low power mode. If the display drive chip detects that the terminal enters the low power mode, a frequency of SWG is decreased to 15 Hz, and frequencies of SWR and SWB are decreased to 1 Hz.

**[0055]** Alternatively, the terminal is already in the low power mode. To be specific, the frequencies of SWR, SWG, and SWB are all decreased from 60 Hz to 15 Hz. In this case, if an instruction on entering an “ultra power saving” mode that is entered by the user is detected, the frequencies of SWG and SWB are both decreased from 15 Hz to 1 Hz, and the frequency of SWR maintains 15 Hz.

**[0056]** FIG. 5(a) is a waveform diagram when the frequencies of SWR, SWG, and SWB are all 15 Hz in an existing low power mode shown in FIG. 1. FIG. 5(b) is a waveform diagram when the frequencies of SWR and SWB are 1 Hz and the frequency of SWG is 15 Hz after the method shown in FIG. 4 is used. Vertical bars on SWR, SWG, and SWB in FIG. 5(a) and FIG. 5(b) indicate drive signals sent in parallel of a plurality of rows (for example, 1092 rows) of subpixels in a frame. SWR is used as an example. Any vertical bar indicates SWR sent of a row of subpixels of a subpixel array.

**[0057]** Based on a control principle, shown in FIG. 1, that the display screen is lit, because the frequencies of SWR and SWB are 1 Hz and the frequency of SWG is 15 Hz, when a signal source (S0, S1, or S2) is at a high level, the red subpixels, the green subpixels, and the blue subpixels are all lit. Therefore, theoretically, a pixel lit on the display screen is displayed in white.

**[0058]** However, the drive frequencies of SWR and SWB are 1 Hz that is less than 15 Hz. Therefore, it is equivalent to a longer time interval between two time points at which the red subpixels and the blue subpixels are lit. Therefore, for a display screen of not good quality, pixels displayed on the display screen may be visually displayed in green.

[0059] Likewise, the frequencies of SWG and SWB may be decreased to 1 Hz, and the frequency of SWR to 15 Hz. Alternatively, the frequency of SWR may be decreased to 1 Hz, and the frequencies of SWG and SWB to 15 Hz.

[0060] Human eyes are more sensitive to green light. Therefore, when the frequencies of the three types of drive signals SWR, SWG, and SWB are decreased, preferably, the frequency of SWG is 15 Hz, the frequencies of SWR and SWB are 1 Hz, to fully use sensitivity of the human eyes to the green light, so that the user does not feel that content displayed on a display screen is excessively dark.

[0061] 1 Hz is only an example. This embodiment of this application does not limit a decreased frequency. Optionally, the decreased frequency is less than 15 Hz. 15 Hz is also only an example.

[0062] Certainly, a lower decreased frequency indicates lower power consumption of the display drive chip. However, a possibility that a pixel displayed on the display screen cannot be displayed in white is higher. During actual application, the first value and the second value may be adjusted based on an actual requirement.

[0063] It can be learned from FIG. 5 that when the frequencies of SWR, SWG, and SWB are different, and the frequencies of SWR and SWB are both lower than the frequency of SWG, normal display of the display screen can still be maintained. It can be teamed that the screen display control method shown in this embodiment can decrease power consumption of a display driver while maintaining the normal display of the display screen.

[0064] FIG. 6 shows another display screen control method according to an embodiment of this application. The method includes the following steps.

[0065] S601: Monitor whether a terminal satisfies a low-power running condition, and if yes, perform S602.

[0066] S602: Turn off any one or two of SWR, SWG, and SWB.

[0067] Optionally, after some drive signals are turned off, a frequency of a drive signal that is not turned off may further be decreased.

[0068] Compared with the foregoing embodiments, in the embodiment shown in FIG. 6, a drive signal is directly turned off, to decrease power consumption of a display driver. After the some drive signals are turned off, subpixels of only some colors are lit, and a color of light emitted by the display screen is a color of a subpixel of the drive signal that is not turned off. For example, SWR and SWB are turned off, and only green subpixels are lit. Therefore, content displayed on the display screen is green. Further, preferably, SWG may be retained, so that a user does not feel that the content displayed on the display screen is excessively dark.

[0069] FIG. 7 shows a terminal according to an embodiment of this application. The terminal includes a display screen, one or more processors, and a display drive chip.

[0070] The display screen is an organic light emitting display screen. As shown in FIG. 1, each pixel unit of the organic light emitting display screen includes at least one red subpixel, at least one blue subpixel, and at least one green subpixel.

[0071] The display drive chip is configured to drive, by using drive signals, the at least one red subpixel, the at least one blue subpixel, and the at least one green subpixel to be lit. For a connection relationship between the display drive chip and the display screen, refer to FIG. 1.

[0072] The processor is configured to monitor whether the terminal satisfies a low-power running condition, and send a control instruction to the display drive chip when it is detected that the terminal satisfies the low-power running condition, where the control instruction is used to instruct to decrease a frequency of any one or two of a drive signal of the red subpixel, a drive signal of the green subpixel, and a drive signal of the blue subpixel.

[0073] In addition to the foregoing functions, the processor may further be configured to monitor whether the terminal satisfies a low-power running condition, and send a control instruction to the display drive chip when it is detected that the terminal satisfies the low-power running condition, where the control instruction is used to instruct to turn off any one or two of a drive signal of the red subpixel, a drive signal of the green subpixel, and a drive signal of the blue subpixel.

[0074] For a specific process of implementing the foregoing functions by the processor, refer to FIG. 3, FIG. 4, or FIG. 6. Details are not described herein again.

[0075] The processor for implementing the foregoing functions may be a central processing unit of the terminal, a processor dedicated for controlling the display screen, a processor integrated into the display drive chip, or a separately disposed processor other than the foregoing processors.

[0076] The processor may be a general purpose processor, a digital signal processor (DSP), an application-specific integrated circuit (ASIC), a field programmable gate array (FPGA), another programmable logic device, a transistor logic device, a hardware component, or any combination thereof. The processor may implement or execute various examples of logical blocks, modules, and circuits described with reference to content disclosed in this application. Alternatively, the processor may be a combination of processors implementing a computing function, for example, a combination of one or more microprocessors, or a combination of the DSP and a microprocessor.

[0077] The functions of the processor may be implemented in a hardware manner, or may be implemented in a manner of executing a software instruction by the processor. The software instruction may be formed by a corresponding software module. The software module may be stored in a RAM memory, a flash memory, a ROM memory, an EPROM memory, an EEPROM memory, a register, a hard disk, a removable hard disk, a CD-ROM, or a storage medium in any other form known in the art. For example, a storage medium is coupled to a processor, so that the processor can read information from the storage medium or write information into the storage medium. Certainly, the storage medium may be a component of the processor. The processor and the storage medium may be located in the ASIC. In addition, the ASIC may be located in the terminal. Certainly, the processor and the storage medium may exist in the terminal as discrete components.

[0078] Parts included in the terminal may communicate by using a bus.

[0079] The terminal shown in FIG. 7 can achieve, by turning off a drive signal of subpixels or decreasing a frequency of a drive signal of subpixels, an objective of reducing power consumption.

[0080] A person skilled in the art should be aware that in the foregoing one or more examples, functions described in this application may be implemented by hardware, software,

firmware, or any combination thereof. A person skilled in the art should easily be aware that, in combination with the examples of units and algorithm steps described in the embodiments disclosed in this specification, this application can be implemented by hardware or a combination of hardware and computer software. Whether a function is performed by hardware or hardware driven by computer software depends on particular applications and design constraints of the technical solutions. A person skilled in the art may use different methods to implement the described functions for each particular application, but it should not be considered that the implementation goes beyond the scope of this application. When this application is implemented by software, the functions may be stored in a computer-readable medium or transmitted as one or more instructions or code in the computer-readable medium. The computer-readable medium includes a computer storage medium and a communications medium, where the communications medium includes any medium that enables a computer program to be transmitted from one place to another. The storage medium may be any available medium accessible to a general-purpose or dedicated computer.

1.-10. (canceled)

11. A display screen control method, comprising:

monitoring whether a terminal satisfies a low-power running condition;

detecting that the terminal satisfies the low-power running condition; and

decreasing a frequency of at least one of a drive signal of a red subpixel of a pixel unit of an organic light-emitting diode (OLED) display screen of the terminal, a drive signal of a green subpixel of the pixel unit, or a drive signal of the a blue subpixel of the pixel unit when the terminal satisfies the low-power running condition.

12. The display screen control method of claim 11, further comprising decreasing a frequency of two of; the drive signal of the red subpixel, the drive signal of the green subpixel, or the drive signal of the blue subpixel, when the terminal satisfies the low-power running condition.

13. The display screen control method of claim 11, wherein detecting that the terminal satisfies the low-power running condition comprises detecting that a battery level of the terminal is less than a preset value.

14. The display screen control method of claim 11, wherein detecting that the terminal satisfies the low-power running condition comprises detecting that the terminal is in a low power mode.

15. The display screen control method of claim 11, wherein detecting that the terminal satisfies the low-power running condition further comprises the terminal receiving a preset instruction.

16. The display screen control method of claim 11, wherein the OLED display screen is an active-matrix OLED (AMOLED) display screen.

17. The display screen control method of claim 11, further comprising further decreasing the frequency to a selected first value.

18. The display screen control method of claim 17, further comprising decreasing a frequency of another drive signal other than a drive signal whose frequency is decreased to the first value to a second value, wherein the second value is greater than the first value.

19. The display screen control method of claim 18, wherein the first value is 1 hertz (Hz), and wherein the second value is 15 Hz.

20. A terminal, comprising:

a display screen, wherein the display screen is an organic light-emitting diode (OLED) display screen, and wherein a pixel unit of the organic light emitting display screen comprises a red subpixel, a blue subpixel, and a green subpixel;

a display drive chip coupled to the display screen and configured to drive, using drive signals, the at least one red subpixel, the at least one blue subpixel, and the at least one green subpixel to be lit; and

a processor coupled to the display screen and the display drive chip and configured to:

monitor whether the terminal satisfies a low-power running condition; and

send a control instruction to the display drive chip when the terminal satisfies the low-power running condition, wherein the control instruction instructs the display drive chip to decrease a frequency of at least one of a drive signal of the at least one red subpixel, a drive signal of the at least one green subpixel, or a drive signal of the at least one blue subpixel.

21. The terminal of claim 20, wherein the processor is further configured to send a control instruction to decrease a frequency of two of; the drive signal of the red subpixel, the drive signal of the green subpixel, or the drive signal of the blue subpixel, when the terminal satisfies the low-power running condition.

22. The Terminal of claim 20, wherein satisfying the low-power running condition comprises a battery level of the terminal having less than a preset value.

23. The Terminal of claim 20, wherein the low-power running condition comprises that the terminal is currently in a low power mode.

24. The Terminal of claim 20, wherein the low-power running condition comprises that the terminal receives a preset instruction.

25. The terminal of claim of 20, wherein the display screen of the terminal is an active-matrix organic light emitting diode (AMOLED) display screen.

26. The terminal of claim 20, wherein when sending the control instruction to the display drive chip, the processor is further configured to send the control instruction to the display drive chip instructing the display drive chip to decrease the frequency of at least one of the drive signal of the at least one red subpixel, the drive signal of the at least one green subpixel, or the drive signal of the at least one blue subpixel to a first value.

27. The terminal of claim 26, wherein the processor is further configured to send a control instruction to decrease a frequency of two of; the drive signal of the red subpixel, the drive signal of the green subpixel, or the drive signal of the blue subpixel when the terminal

decrease a frequency of at least one of a drive signal of the at least one red subpixel, a drive signal of the at least one green subpixel, or a drive signal of the at least one blue subpixel when the terminal satisfies the low-power running condition.

\* \* \* \* \*

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摘要(译)

终端的显示屏为有机发光显示屏,且该有机发光显示屏的每个像素单元至少包括一个监视终端是否满足低功率运行条件的显示屏控制方法及终端一个红色子像素,至少一个蓝色子像素和至少一个绿色子像素,并降低红色子像素的驱动信号,绿色子像素的驱动信号和蓝色的驱动信号中的任一个或两个的频率 当检测到该终端满足低功率运行条件时,该子像素为低像

